



HORI

U. S. Application No. 10/716,678

Our Ref. 8004-1015

The present application relates to a data transmission system for transmitting a decoder and serial data for converting to parallel data, an encoder and serial data for converting parallel data to serial data.

The encoder is technically characterized by the fact that, the change point counter, in respective change points for the change points of n bit data (n : positive integer), makes the changing value of adjacent bits to be numbers, and calculates the numerical results, and if with the changing point counter, it is determined that the numerical results exceed a pre-determined value, discrimination bits becoming the true value are output; and with the code converter, in the case where the discrimination bits are determined to be a true value, in a format in which bits of n bit data positioned in a pre-determined position are inverted, the n bit data is converted, the parallel serial converter becoming such that the discrimination bits in the output of the code converter are added and the created $(n+1)$ bit data is converted to $(n+1)$ bit serial code, with the encoder controlling the EMI by reducing the change point of the serial data, and effectively controlling the high frequency components of the transmitted data.

It is recognized that this falls within a scope in which it could be easily achieved by one skilled in the art from the Citation relating to a data block coding device and method wherein, in the case of counting the number of transitional bits among the data bits, if the number of transitional bits is greater than a determined standard value, the EMI influence is reduced by the inversion of selectively input data bits.

Addenda

Add. 1: Citation Invention W09713348 (04/10/1997)

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